CALCULATOR CHIPS...

MOS Technology has long been considered a technological leader when it comes to the design and mass production of devices used in OEM quantities by large manufacturers of calculators. Several new devices have recently been announced, however, the MCS 7529-103 may be the most attractive to small quantity users of the 6502 in view of its low cost in small quantities and off-the-shelf delivery. New pricing on the MCS 7529-103 is $16.50 in lots of 1 to 99.

Ohio Scientific Instrument will soon make available their Model 450 firmware basic board, and it will use the 7529-103 calculator chip for all arithmetic functions. Additional information on the device follows:

The 40 Key single chip calculator Array MPS 7529-103 operates in conjunction with a twelve digit display and a 40 key keyboard to allow execution of both arithmetic and transcendental functions.

Operating features of the system include:
- Number entry in floating point or scientific notation.
- Automatic selection of correct notation for result display (scientific or floating point).
- Algebraic problem entry.
- Two parenthesis levels.
- 8 digit mantissa.
- Sign selection and display.
- Full chain calculation with any function sequence.

Functional features of the system include:
- Basic arithmetic (+,-,x,÷)
- Trig Functions (sin, cos, tan)
- Inverse trig functions($\sin^{-1}$, $\cos^{-1}$, $\tan^{-1}$)
- Logarithms (ln and log)
- Anti-logarithms (e^x and 10^x)
- Exponentiation (y^x)
- Factorials (n!)
- Convenience functions ($1/x$, x^2, $\sqrt{x}$,)
- Full feature memory (STO, RCL, M+)
- Exchnge operations (xy)
- Degree radian selection (Trig or Inverse Trig)
- Automatic error detection and display
- Clearing operation and display (CA, CE)

Electrical features of the system include:
- Minimum system power consumption.
- Direct LED segment drive.
- Internal display cutoff (for battery life extension; no external components).
- Internal clock oscillator and drivers. (No external components.)
- Internal power-on-clear circuit (no external components).
- Internal key validation, debouncing, and noise rejection (no external components.)

This array is manufactured with a low threshold P-Mos process utilizing ion implantation techniques and is supplied in a 28 pin dual-in-line package.

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Barry Luokkala
Department of Physics
Carnegie Mellon University

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